## **PDA-604A Sifos PoE Powered Device Analyzer** echnologies IEEE 802.3bt & 802.3at Power-over-Ethernet PRELIMINARY Sterr Tolata Product Overview Keep Pur Ohishen sheeing pont Deriv Taor Sifos i LCE Shart PDA-604A 0 400 Sifos Patented Technology from Sifos

# **Key Features**

- □ 4-Pair and 2-Pair Powering and Analysis of All 802.3bt / 802.3at PD's
- □ Fully Configurable Classification and 802.3bt / 802.3at Power Grants
- □ Continuous PD Powering to >82 Watts at All PD Input Voltages
- **Comprehensive** Analysis of 802.3bt / 802.3at PD Performance Parameters
- □ Automated IEEE 802.3at\* Powered Device Conformance Testing
- □ Flexible 802.3at\* LLDP Emulation and LLDP Protocol Analysis
- **Powerful** Metering: Voltage, Current, & Power Sampling at the PD Interface
- □ Intuitive Graphical User Interface for Rapid Analysis and Testing
- □ Powerful Script Automation and Binary API Library for Microsoft Windows
- □ Informative Pop-Up Spreadsheet Reports and Statistics
- □ Plug'n Play USB Interface to Windows PC's
- LAN Port for External PD Configuration and Control During Testing



# **One Box Solution**

- Replaces PSE's, DC Supplies, Fixtures, Scopes, Meters, & Protocol Analyzers
- Just Plug and Test

# IEEE 802.3bt & 802.3at PD's

- Type-1 (≤13W) PD's
- Type-2 (≤25.5W) PD's including LLDP Power Negotiation
- Type-3 (≤51W) PD's
- Type-4 (≤71.3W) PD's

# Assure Full Interoperability

- Emulate 4-Pair and 2-Pair Powering
- Emulate Type-1, 2, 3, and 4 Power Grants to All PD's
- Real-Time Load Monitoring
- Automatic Static and Transient Load Limit Violation Analysis
- Automated 802.3at\* PD Conformance Testing including LLDP
- Configurable Waveform Traces Including Class, Source, Transient Triggering
- 802.3at\* LLDP Power Negotiation Protocol Verification

## Versatile Applications

- Evaluation & Design
- Quality Assurance
- Manufacturing Test
- Field Service
- Energy Standard Rating

# Verification, Simplified.

### **Overview**

The PDA-604A Powered Device Analyzer is a single-box comprehensive solution for testing **IEEE 802.3bt** and **802.3at** PoE Powered Devices (PD's). It offers one-button, fully automated test sequences\* and limit checking for critical Powered Device PoE characteristics. With measurements performed at the Powered Device network interface, many parameters critical to 802.3at and 802.3bt interoperation can be accurately assessed relative to specification requirements, thus fully avoiding the need for and the severe limitations associated with 802.3at/802.3bt PSE's.

### Fully Integrated, One-Box Solution

The PDA-604A removes the need for specialized instrumentation setups requiring DC power supplies, precision meters, custom test fixtures, protocol analyzers, a variety of PSE's, and custom software. The PDA-604A can be used with PDA Interactive software to perform specification compliance analyses of new PD designs and to troubleshoot PD specification compliance problems. The PDA-604A can facilitate remote configuration of PD states over the LAN while simultaneously assessing power demand and LLDP\* processing from a PD. Different PSE behaviors can readily be mimicked including detection cycling, single and multi-event classification with and without elongated first class events, class-to-power timing, and LLDP\* acknowledgement timing. The PDA-604A includes robust automation development facilities including Tcl/Tk scripting and binary API libraries. This versatility allows users to apply the PDA-604A over the full lifecycle of any Powered Device including newer, Type-3/4, IEEE 802.3bt compliant PD's.

### Superior Defect Coverage

The PDA-604A provides defect coverage far beyond what a commercial PSE or instrument grade DC power supply might offer. It provides power and performs measurements in all possible 2-pair and 4-pair connection and polarity configurations. Measurements including DC load-over-voltage, classification validity, power on-off thresholds, MPS validity, and detection characteristics are readily performed and compared to applicable specification limits. Load currents up to 1A per pairset, or 2A total, can be sourced and sensed with PD input voltages ranging from 28 to 57VDC. Sporadic transient loads can be captured with sampling resolution as granular as 200µsec. The PDA-604A test port can link to any PD at 10Base-T, 100Base-T, or 1000Base-T and it can optionally relay multi-gig links up to 10GBase-T between a PD and an external network device.

### Flexible Automated Testing of 802.3at PD's

The PDA-604A offers an optional 802.3at PD Conformance Test Suite\* and associated reporting that may be further optioned for Type-1 or Type-1 & 2 PD testing, including PoE LLDP protocol testing. This "one-button" test suite produces over 50 limit-checked PD parameters. The test suite is accessible from the PDA Interactive (GUI), from the PowerShell PD scripting environment, or from any programming language interfacing Windows DLL's. Test results are produced as colorful Microsoft Excel spreadsheets to annotate problem areas and provide multi-cycle statistics.

### Powerful Real-Time Load Monitor and Compliance Analysis

Under PDA Interactive software, the PDA-604A offers powerful real time tools for analysis of PD power draw over arbitrary periods of time under constraints of user-specified PD power grants, including both class-based and LLDP\* grants. Real time limit checking of average, peak, transient, and MPS power is performed.

### **Desktop Ready Design**

The PDA-604A is at home on any desktop or lab bench with USB to host PC connectivity and a cooling fan that only runs when powering PD's.

\* Future PDA-604A options will include 802.3bt Conformance Test and 802.3bt LLDP Emulations

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## PDA-604A Versus a Commercial PSE

With the ready availability of commercial Power Sourcing Ethernet Switches (PSE), including low cost PSE injectors, a strong temptation exists to utilize these products to test Powered Devices. Coupled with a long spool of cable, a PSE provides a "real world" interface to a PD.

As an "interop" test strategy, this approach overlooks the wide-ranging design flexibility allowed to IEEE **802.3bt** and **802.3at** PSE's. This attribute of the PoE standard has translated into a vast proliferation of PSE designs and configurations with widely varying tolerances of many critical PD traits. PD's that interoperate with one or a few PSE's may fail to properly interoperate with hundreds of other specification compliant PSE's and cabling networks.

The reality is that PSE's are not test instruments. A PSE cannot test critical characteristics of a PD that are vital to interoperability over all PoE networks. Even the most sophisticated PSE's that offer some management reporting of PD classification and power draw offer no insight regarding how the PSE produces those parameters or what they might really mean.

Table 1 illustrates a variety of PD performance parameters that are critical to the broad interoperability of a PD and the respective test coverage that can be expected from a commercial PSE relative to a PDA-604A.

## PDA-604A Feature Scalability

The PDA-604A is a scalable instrument for testing IEEE 802.3bt and 802.3at PD's. This allows users to choose the best configuration at the lowest possible cost to suit their needs.

| PD Behavior                                     | PDA-602<br>Test<br>Coverage | Commercial<br>PSE Coverage |
|---|-----------------------------|----------------------------|
| PD Power-Ups to Minimum / Maximum Voltages      | ✓                           | ×                          |
| Ethernet LAN Link-Up / Auto-Neg / Rate Control  | 1                           | ?                          |
| ALT-A, ALT-B, & 4-Pair Powering                 | 1                           | ×                          |
| MDI & MDI-X Powering Permutations               | 1                           | ×                          |
| Detection Resistance – Single & Multi- Cycle    | ✓                           | ?                          |
| Detection Resistance vs Voltage*                | ✓                           | ×                          |
| Detection Capacitance – Single & Multi-Cycle    | ✓                           | ×                          |
| Connection Check/Signature Validation           | ✓                           | ?                          |
| Classification Signature (per Pairset)          | ✓                           | ?                          |
| Classification Signature Per Class Event        | ✓                           | ×                          |
| Classification Signature vs Voltage*            | ✓                           | ×                          |
| Mark Loading                                    | 1                           | ×                          |
| Inrush Loading (per PSE Type-1, 2, 3, and 4)    | ✓                           | ×                          |
| Inrush Limiting (per PSE Type-1, 2, 3, and 4)   | ✓                           | ×                          |
| Type-2/3/4 Power Delay                          | ✓                           | ×                          |
| Turn-On Voltage                                 | ✓                           | ×                          |
| Turn-Off Voltage                                | ✓                           | ×                          |
| Average Power Consumption (per Class Grant)     | ✓                           | ×                          |
| Instantaneous Peak Power Load (per Class Grant) | ✓                           | ×                          |
| Windowed Peak Power Load (per Class Grant)      | ✓                           | ×                          |
| Classification Integrity                        | ✓                           | ×                          |
| MPS – Level (per PSE Type 1, 2, 3, and 4)       | <ul> <li>✓</li> </ul>       | ?                          |
| MPS – Duty Cycle (per PSE Type 1, 2, 3, and 4)  | ✓                           | ?                          |
| Load Power over Voltage                         | 1                           | ×                          |
| LLDP Message Formatting                         | ✓                           | ?                          |
| LLDP Allocation Response Time                   | ✓                           | ×                          |
| LLDP Requested Power Integrity                  | ✓                           | ×                          |

 Table 1: PDA-604A versus Commercial PSE Coverage

The base configuration of the PDA-604A enables both 2-Pair and 4-Pair PD powering, power-ups and metering that provide between zero and three classification events, detection measurements that include 802.3bt connection check, and highly programmable waveform captures of voltage, current, and power in 2-Pair or 4-Pair modes. This configuration supports testing of **802.3at PD's** with PSE emulations that include both **802.3at** and **802.3bt PSE's**.

Table 2 depicts licensed feature options for testing high power PD's, automated test suites, and LLDP emulation and analysis. These are further described in the sections that follow Table 2.

| Feature<br>Option   | Description   | Features I<br>Load Monitor<br>(2-Pair & 4-Pair) | ncluded<br>Class 5-8,<br>Class 4-5D | Required<br>Features                   |
|---------------------|---|---|-------------------------------------|--|
| TYPE-3/4            | Type-3/4 4-Event and 5-Event Power-Ups to<br>support Class 5-8 and Dual Class 4-5 PD's              |   | <b>√</b>                            |  |
| CT-AT               | 802.3at Type-1/2 PD Automated Test Suite, Load<br>Monitor & Streaming Traces for up to Type-4 PD's  | $\checkmark$                                    |                                     | <b>Type-3/4</b> for<br>Load Mon>Type-2 |
| LLDP-AT             | Type-1 LLDP & Type-2 LLDP PSE Emulation &<br>Protocol Analysis                                      |   |                                     |  |
| CT-AT +<br>LLDP-AT  | Type-1, Type-2 2-Event, & Type-2 LLDP PD<br>Automated Test Suite, Load Monitor with "at" LLDP       | $\checkmark$                                    |                                     |  |
| CT-BT*              | 802.3at Type-3/4 PD Automated Test Suite  | $\checkmark$                                    | $\checkmark$                        | Type-3/4,<br>CT-AT                     |
| LLDP-BT*            | 802.3bt Type-3/4 LLDP PSE Emulation & Protocol Analysis   |   | $\checkmark$                        | Type-3/4,<br>LLDP-AT                   |
| CT-BT +<br>LLDP-BT* | Type-3, Type-4 Multi-Event, & Type-3/4 LLDP PD<br>Automated Test Suite, Load Monitor with "bt" LLDP | $\checkmark$                                    | $\checkmark$                        | Type-3/4,<br>CT-AT, LLDP-AT            |

Table 2: PDA-604A Feature Options and Combinations. \*

\* Future Product Feature.

## Type-3/4 PD Testing with the PDA-604A



Figure 1: 3-Event Classification

The base configuration of the PDA-604A supports 2-Pair and 4-Pair powering and discrete measurements. With the advent of the IEEE 802.3bt standard, many new PSE's will apply 4-Pair powering whether they are powering 802.3at (Type-1/Type-2) PD's or newer 802.3bt Type-3/Type-4 PD's. The PDA-604A base configuration can simulate the detection, classification, and powering

characteristics of both 802.3at and 802.3bt PSE's as they interact with 802.3at (Type-1/Type-2) PD's. PD classification can be configured to 0, 1, 2, or 3-Events (see Figure 1) meaning Type-2 PD's can be granted full 25.5 watt power levels via 2-Event or 3-Event classification. Additionally, the first class event may be specified to use the 802.3bt elongated (LCE) class pulse or the "normal" 802.3at compliant class pulse.

Figure 2 shows a 4-Event

50.0

The Type-3/4 feature option extends the classification capability of the PDA-604A to include 4-Event and 5-Event classification required to grant power levels beyond 25.5 watts to 802.3bt compliant PD's. Under the 802.3bt standard, PD's that operate at more than 25.5 watts normally are restricted to draw less than 25.5 watts given 2-Event or 3-Event classification and less than 13 watts given 1-Event classification.





trace of voltage and current to a Class 8 PD drawing just under 1.2 amps of load current. This particular trace, triggered on the start of PD classification, is made possible by the Type-3/4 feature option.



Figure 2: 4-Event Class Meter Measurement

> Sifos chnologies

> > 1 2000

1,0000

s00.0 3 600.0

400.0

200.0

00

classification signature measurement for a Class 6 PD and Figure 3 is a current waveform depicting a 4-Event classification sequence to a Class 6 PD. Measuring 802.3at and 802.3bt PD performance under conditions of "power

8.783 0.19 46.4 0.5 01 507.4

4 pair Voltage+Current Trace

demotion", that is with various classification event counts. is an essential form of interoperability testing for all Class 4 and higher PD's.

Figure 4 is a 4-Pair power-up

> At B (VDC) Figure 4: Class 8 PD Power-Up V+I Waveform

ALA (VDC)

## The 802.3at PD Conformance Test Suite & Load Monitor (CT-AT)

The 802.3at PD Conformance Test Suite is a fully automated 802.3at specification compliance test suite for a PD. The test suite performs many measurements of PD interface parameters that are critical to interoperability with the full range of compliant 802.3at PSE's and connection environments. Testing can optionally be configured to run on a single guadrant (e.g. Alt-A. MDI) or on up to 4 guadrants (Alt-A and B. MDI and MDI-X). Measurements are organized into passive pre-powered parameters and powered state parameters. The test suite automatically produces color-coded Microsoft Excel spreadsheets that are organized by guadrant and test category (see Figure 5).

Test coverage is provided for Type-1 (13W) PD's and Type-2 (25.5W) PD's. Test coverage can be expanded to include Type-2 PD's responding to PoE LLDP protocols and PSE power grants with the addition of the LLDP-AT Emulation and Analysis feature described below.

### Certified for 1<sup>st</sup> Party EA Logo Testing

The 802.3at PD Conformance Test Suite is certified to support 1st party, or in-house, Ethernet Alliance (EA) PoE logo testing. PD manufacturers seeking to perform 1<sup>st</sup> party testing may utilize the EA Cert Mode available with the PD Conformance Test Suite to run fully automated testing that produces specialized EA test reports required to obtain and update PoE logo certifications for their powered devices.



One of the most critical operating parameters of a powered device is the load power consumed as the device operates in a number of states and under a number of varving conditions. In many instances, the maximum and minimum power consumption levels of a PD cannot be ascertained without over-the-network interactions. Common examples include wireless access points that consume power based on numbers and proximities of wireless users, IP cameras consuming transient power when panned or zoomed in harsh weather conditions, and IP telephones altering power consumption based on server enabling, video display states, and even network interface speed.

The **CT-AT** option enables a powerful **Load Monitor** (*see Figure 6*) offering the capability to continuously monitor instantaneous and average power consumption of a PD over long periods of time while operating conditions of the PD are manipulated. The Load Monitor is accessed from PDA Interactive software. It includes the intelligence to evaluate both static and transient power excursions that may violate 802.3at limits and ultimately cause PSE's to remove power from a PD unexpectedly. Static load power is evaluated to PD advertised physical layer classification or optionally to PD LLDP power request levels. Transient load power is automatically evaluated to peak instantaneous



Figure 6: PDA-604A Load Monitor

A third feature of the CT-AT option is the ability to **stream long traces** of instantaneous and average power consumption into spreadsheet reports (*see Figure 7*) and data files for subsequent analysis. Streaming traces can collect power consumption samples with sample granularity as small as 5msec over many hours. As with the real-time Load Monitor, the streaming trace report can identify and localize power violations and also report DC MPS (low current) violations.

#### **Combining the Type-3/4 Feature with the CT-AT Feature** Because the **CT-AT** feature enables the **Load Monitor** and **Streaming Traces** in combination with the **Type 3/4** feature

Streaming Traces, in combination with the Type-3/4 feature option, the both the Load Monitor and Streaming Trace resources

PDA-600 TEST REPORT MD ¢ Sifos tection & Classificat MDI Parameter Units Rdet final t\_unpwi Cdet\_final 1 Event Classification 40.1 40.1 mA 40. 40.1 40.1 36.0 44.0 lass lassNum class ClassStability Event Class class\_event1 0.000 sec 0.000 0.000 0.000 0.000 ass event larkl lassNum2 0.000 0.00 0.000 0.00 0.000 er-Up / Do iax\_Tdelay ish delayed red Type-2 P NOTE: Type-2 testing did not include LLDP, so PD Data Link Layer characteristics were not checked

Figure 5: PD Conformance Test Report

loading limits and to windowed transient limits that are enforced by PSE's. These parameters are also derived from PD advertised classification and any LLDP power requests.

The **Load Monitor** is the natural tool for developing assurance that the PD classification (and any PD LLDP power request level) is compliant with actual PD behavior under all operating conditions and for troubleshooting PD's that experience unexpected shutdowns while in service. As with the PD Conformance Test Suite, the Load Monitor can be extended to utilize PoE LLDP (*for 802.3at*) to acquire and set limits in accordance with PD LLDP power requests and PSE power allocations, given the **LLDP** feature option.





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become available for analyzing 802.3bt Type-3 and Type-4 PD's including those drawing power to 71 watts or higher. That means that limit checking for PD average, peak, transient window, and MPS violations is available when evaluating **802.3bt Class 5-8** PD's.

In Figure 8, a 60 second duration streaming power trace is captured from a Class 6 PD drawing just under 50 watts continuously following various start-up transients. No power violations are flagged during this trace. In Figure 9, a 12 second streaming trace from a Class 7 PD is captured indicating both a peak power and a transient power window violation.





Figure 8: Streaming Power Trace from Class 6 PD

Figure 9: Streaming Power Trace from Class 7 PD

### PoE LLDP Emulation and Analysis\* with the PDA-604A (LLDP-AT)

A mandatory requirement of Type-2 PD's under the IEEE **802.3at** specification is that they support 802.3at PoE extensions to LLDP (link layer discovery) protocol. Many Type-2 PSE's do not provide 2-Event classification but instead, rely on LLDP message exchanges to learn the power demand of a Type-2 PD, then if the power is available, to grant that power demand in the form of a power allocation. This then allows the Type-2 PD to draw power levels up to the communicated power demand. It also allows the PSE to manage power budgets with 0.1 watt power precision per PSE port. Many larger (24 port and higher) managed Type-2 PSE's implement PoE LLDP in order to best utilize shared power

resources and assure stable powering to all powered PD's.

With the **LLDP-AT** feature option, the PDA-604A can flexibly mimic Type-2 PSE's that deploy PoE LLDP. While emulating userdefined PSE LLDP behaviors, the PDA-604A can collect and analyze PoE LLDP protocol and report any



Figure 10: LLDP Power-Up Protocol Trace Report

specification violations within that protocol exchange. Figure 10 depicts a specification-compliant LLDP exchange following the power-up of a Type-2 PD that demands 22.3W power. Figure 11 captures LLDP messaging from a PD that is slow to respond to a PSE's power allocation and therefore produces a protocol timing violation.

| ember 2<br>PSA Add | 2015<br>Iress: 6 | 04AENG9 | 11:18 AM | 1        | Trace Type: | Pow     | /er_Up   |           |           |            | version 4.1.01 | Technolo  |
|--------------------|------------------|---------|----------|----------|-------------|---------|----------|-----------|-----------|------------|----------------|-----------|
| Time               |                  | From    | То       | PD Class | Туре        | Source  | Priority | Requested | Allocated | Port Class | MDI Support    | MDI State |
|                    | 5.0              | PD      | PSE      | 4        | 2           | PSE     | LOW      | 13.0      | 13.0      | PD         | N/A            | N/A       |
|                    | 9.0              | PSE     | PD       | 4        | 2           | PRIMARY | LOW      | 13.0      | 13.0      | PSE        | YES            | ON        |
|                    | 10.0             | PD      | PSE      | 4        | 2           | PSE     | LOW      | 13.0      | 13.0      | PD         | N/A            | N/A       |
|                    | 14.0             | PD      | PSE      | 4        | 2           | PSE     | LOW      | 24.3      | 13.0      | PD         | N/A            | N/A       |
|                    | 18.0             | PD      | PSE      | 4        | 2           | PSE     | LOW      | 24.3      | 13.0      | PD         | N/A            | N/A       |
|                    | 18.0             | PSE     | PD       | 4        | 2           | PRIMARY | LOW      | 24.3      | 24.3      | PSE        | YES            | ON        |
|                    | 22.0             | PD      | PSE      | 4        | 2           | PSE     | LOW      | 24.3      | 13.0      | PD         | N/A            | N/A       |
|                    | 25.0             | PSE     | PD       | 4        | 2           | PRIMARY | LOW      | 24.3      | 24.3      | PSE        | YES            | ON        |
|                    | 27.0             | PD      | PSE      | 4        | 2           | PSE     | LOW      | 24.3      | 13.0      | PD         | N/A            | N/A       |
|                    | 30.0             | PD      | PSE      | 4        | 2           | PSE     | LOW      | 24.3      | 13.0      | PD         | N/A            | N/A       |
| 1.1                | 34.0             | PD      | PSE      | 4        | 2           | PSE     | LOW      | 24.3      | 24.3      | PD         | N/A            | N/A       |

Protocol traces such as these are easily captured and reported in colorful Excel spreadsheet reports that annotate any protocol violations or limitations. Power-Adjust protocol traces are also readily captured to analyze PD responses to delayed power grants and to PSE power throttle-back requests.

Figure 11: LLDP Power-Up Trace – PD Timing Violations

\* PoE LLDP Emulation and Analysis for 802.3bt Type-3/4 PD's will be a future license option with the PDA-604A.

Combining the **LLDP** feature option with the **CT-AT** feature option creates powerful capabilities whereby LLDP power-up negotiations become an integral part of the Load Monitor and the PD Conformance Test Suite. The Load Monitor can now respond to LLDP-supervised Type-2 power-ups and automatically adjust average and peak power

| Powered Type-2 LLDP               |       | Emulation | On Time: | 60 sec | Off Time. | 10 sec   |           | 42.5  |
|-----------------------------------|-------|-----------|----------|--------|-----------|----------|-----------|-------|
| Parameter Cycle:                  |       | Units     | Mile.    | Max.   | Average   | Low Lin. | High Lim: | 917   |
| Mail PreAlloc                     | 224.0 | Am        | 224.0    | 224.0  | 224.0     | 0.0      | 343.6     | P     |
| Maid PreAlloc                     | 200.6 | mA        | 200.6    | 300.6  | 209.0     | 10.0     | 343.6     | 1     |
| Vport_PreAlloc                    | 41.9  |           | 41.9     | 41.9   | 41.9      | 42.5     | 57.0      | INFO  |
| Ppeak_PreAlloc                    | 16.3  | w         | 16.0     | 16.3   | 16.3      | 0.0      | 14.4      |       |
| Pavg_PreAlloc                     | 14.9  | w         | 14.9     | 14.9   | 54.9      | 0.0      | 13.0      | 1     |
| MPSViolation_PreAlloc             | 0     |           |          | 0      | -         | 0        | 0         | P     |
| TcutWindowViolation_PreAlloc      |       |           |          | 1      | -         | 0        | 0         |       |
| DutyCycleViolation_PreAlloc       |       |           |          | 1      |           | 0        | 0         | 1     |
| Mini_PostAlloc                    | 277.9 | Am        | 277.9    | 277.9  | 277.9     | 0.0      | 675.5     | P.    |
| Maxi_PostAlloc                    | 371.4 | mA        | 371.4    | 371.4  | 371.4     | 10.0     | 675.5     | - P.  |
| Vport_PostAlloc                   | 41.9  |           | 41.9     | 41.9   | 41.9      | 42.5     | 57.0      | INFC  |
| Ppeak_PostAlloc                   | 15.6  | w         | 15.6     | 15.6   | 15.6      | 0.0      | 20.3      | P     |
| Pavg_PostAlloc                    | 13.5  | W         |          | 13.5   | 13.5      | 0.0      | 25.5      | P     |
| MP5Violation_PestAlloc            | ő     |           | 6        | ¢      | -         | 0        | 0         | P     |
| TcutWindowViolation_PostAlloc     | 0     |           | 0        | 0      | -         | 0        | 0         | P     |
| DutyCycleViolation_PostAlloc      | Ő     |           | 0        | Ó      |           | 0        | 0         | P     |
| Mini_AterThuttle                  | 0.0   | mA        | 0.0      | 0.0    | 0.0       | 0.0      | 376.3     | INFO  |
| Maxl_AterThrottle                 | 366.8 | Am        | 366.8    | 366.8  | 366.8     | 10.0     | 376.3     | 000   |
| Vport_AfterThrottle               | 41.9  |           | 41.9     | 41.9   | 41.9      | 42.5     | 67.0      | INFO  |
| Ppeak_AtterThruttle               | 15.4  | w         | 15.4     | 15.4   | 15.4      | 0.0      | 14.4      | INFC  |
| Parg_AterThrottle                 | 13.5  | w         | 13.5     | 13.5   | 13.5      | 0.0      | 13.0      | INFO  |
| MPSViolation_AfterThrottle        | 1     |           |          | 1      |           | 0        | 0         | INFO  |
| TcutWindowViolation_AfterThrottle | Ó     |           | 0        | 0      |           | 0        | 0         | INFO  |
| DutyCycleViolation_AtterThrottle  | 1     |           | 1        | 1      | -         | 0        | 0         |       |
| LLDP Protocol Characteristics     |       |           |          |        |           |          |           |       |
| Parameter Cycle:                  |       | Units     | Min.     | Max.   | Average   | Low Lim. | High Lim. | PIF   |
| Time ToLink                       | 47.0  | SEC       | 47.0     | 47.0   | 47,0      |          |           | INFO  |
| LinkSpeed                         | 1000  | rate      |          |        | -         |          | _         | INFO  |
| FestRegTene                       | 47.0  | sec       | 47.0     | 47.0   | 47.0      |          |           | 100-0 |
| PoverRequest.                     | 21.6  | W         | 21.6     | 21.6   | 21.6      | 0.1      | 13.0      | P     |
| PDAckTime                         | 7.4   | SIEC      | 7.4      | 7.4    | 7.4       | 0.0      | 10.0      | P     |
| AllocPowerEchoed                  | 21.6  | W.        |          | 21.6   | 21.6      | 0.1      | 25.5      | P     |
| ThrottleAckTime                   | 2.0   | sec       |          | 2.0    | 2.0       | 0.0      | 10.0      | P     |
| ThruttlePowerRea                  | 26.6  | w         | 36.6     | 36.6   | . 26.6    | 0.1      | 26.6      | INFO  |

Figure 13: Test Suite with LLDP

limit lines according to PD Requested (and PSE Allocated) power levels (*see Figure 12*). This makes the Load Monitor a powerful tool for assessing PD LLDP power request levels.

With LLDP, the PD Conformance Test Suite adds

coverage for LLDP protocol



messaging and PD initial post-power-up power consumption prior to and following the LLDP negotiation (*see Figure 13*). As with the Type-2 feature, **LLDP**\* is an essential feature for fully evaluating Type-2 PD's to all applicable requirements.

### PDA Interactive Software

The PDA-604A is a software-managed instrument. The user interface to the instrument is host-based software running on a Windows PC. **PDA Interactive**, a component of PDA-600 software, is an intuitive graphical user

| Front End Config<br>Connection<br>C At A<br>C At B   | Trace<br>Votage C Stream<br>C Current E Load Monitor   | Meter C Voltage C Current  |
|--|--|--|
| A Pair (PDA-604)      At A Polarity     MDI    MDIX      At B Polarity     MDI    MDIX      MDI    MDIX  | C Power E Re-use Class:<br>C Voltage+Current PD Class: ?<br>Stream Len. (sec) 30 4<br>Sample Rate (ms) 1 1<br>Trace Len. (samples) 128 5 | C Power<br>Backleed/<br>Rdet & ConnChk.<br>Rdet Unpwr<br>C Cdet  |
| Source<br>Vpot Level Power Slew<br>[41.1 ⊕ G Immediate<br>C Ramp<br>Class Events Mask5(m)<br>[56:vents ] 50 ⊕<br>POA-604 Settings<br>Classify On C At A+8<br>G AAA C AK 8<br>Class Event 1 Width | Trigger [Immediate ➡]<br>Holdoff (sampleta) 03<br>Trace Duration: 0.128 sec limit. 4 pair<br>Result                                      | Court 1 S<br>Court 1 S<br>Mode Find<br>Find<br>Find<br>STATUS READY<br>ALT ARESISTANCE: 25:30<br>ALT BRESISTANCE: 25:30<br>MODE SIGNATURE_TYPE: SINGLE |
| LCE C Shot     A to B Power     Stagger (ms)     Class & Pwt     Salect EDA  | End  | Measure Margin Test  |
| 2.604A0001 -   | PDA Config   | Exit   |

Figure 14: Analyze Menu Rdet Measurement

Test Suite LLDP Passive Tests ed Tests T All Quadrants T All Quadrant IF Type 2 Phy IF Type 2 LLDF ALT A. MDI ALT A, MDI ALT A. MDIX ALT B. MDIA ALT B, MDIA Type 1 48.0 1 Powered Phy 10 3 Test Cycles 1 Type 2 54.0 1 Unpowered (sec) 10 🖶 DUT Info. di+altbridix cycles 1 multi 1 pvr altamdix type1 ph\_\_\_\_ ard Time 2015 vole 1 for PDA\_ALT\_A\_ 1 for PDA\_ALT\_A\_MDI Start Test Stop Test View Report Exit tatus: Source OFF



interface that can access all of the key features and capabilities of the PDA-604A.

PDA Interactive provides three file-tabbed menus:

**Analyze**: The Analyze menu (*see Figure 14*) supports interactive powering, metering, and waveform trace captures. With the **CT-AT** feature option, it adds access to the Load Monitor and stream tracing features of the PDA-604A. The **Type-3/4** feature option further enhances this menu by allowing 4-Event / 5-Event powerups and PD Class measurements (*see Figure 15*). In general, the

Analyze menu enables intuitive methods of manually testing and analyzing many essential characteristics of a PD.

| Front End Config   | Trace  | Meter  |
|--|--|--|
| Correction<br>C AB A<br>C AB A<br>C AB A<br>C AB B<br>C 4 Pair (POA 604)<br>AA Polarly<br>C MOI ← MOI×<br>AB Polarly<br>C MOI ← MOI×<br>Souce<br>Voot Level Mot ← MOI×<br>Souce<br>C Bare Correction<br>C Bare Correction<br>C AB A<br>C AB A<br>C AB A<br>C AB A<br>C AB A<br>C AB Power Sleve<br>C AB C<br>C C C C<br>C AB C<br>C C C C | Volage     Volag | Current Carrent Content Conten |
| Select PDA   |  |  |

**Test Suite**: The Test Suite menu shown in Figure 16

Figure 15: Analyze Menu Class Measurement

is available to instruments with the **CT-AT** feature option. This menu provides for configuration and control of the 802.3at PD Conformance Test Suite. Users can select quadrants (Alt-A,B and MDI,MDI-X) for both unpowered and powered state testing, source voltage levels by PD type, and test coverage options. Test coverage options include **Type-1 Phy** PD, **Type-2 Phy** PD, and **Type-2 LLDP** if the **LLDP-AT** feature option is enabled. **LLDP**: This menu accesses the PSE LLDP emulation and LLDP protocol tracing features of a PDA-604A given that the instrument is enabled with the **LLDP-AT** feature option. PSE LLDP emulations allow configuration of PSE-controlled message fields, power (available) allocation, power grant logic, transmit period, and response delay between new PD power request values and PSE acknowledgement of those updated values. LLDP trace types include Power Up Trace for evaluation of initial PD LLDP negotiation and Power Adjust Trace for evaluating PD responses to revised PSE power allocations after power-up.

## **PowerShell PDA Software**

PDA-600 software provides a robust, Tcl/Tk-based script development environment consisting of intuitive commands for configuring PDA-604A resources, performing measurements, running PD Conformance Tests, Load Monitor streams, and LLDP protocol traces. PowerShell



PDA-604A Powered Device Analyzer

#### Figure 17: PDA Interactive LLDP Menu



PDA Wish Console in Figure 18: DoworShall DDA Wish Console

Figure 18: PowerShell PDA Wish Console

Shell supporting typical Windows editing operations. It also enables Tk graphical user interface commands along with Tcl and PDA-600 commands.

The PowerShell PDA Tcl Console in Figure 19 is the Windows command prompt shell environment extended with Tcl commands and PDA-600 commands.

test scripts for volume QA or manufacturing is a very natural application for PowerShell PDA. When PDA-600 software is installed, two forms of interactive command consoles are offered with corresponding desktop icons. The PowerShell

PDA supports interpreted, immediate execute commands and queries from a command shell with the ability to build automated test scripts

using both PDA commands and the wealth of programming commands

available with Tcl/Tk. Scripting and debugging dedicated, customized

| DeviceNun<br>1<br>2   | SN<br>60280002<br>604A0001                                  | PDA_FW F<br>2.03              | DA_HV US            | B_HW SP<br>? ?<br>6 HI | EED MOD            | EL LAN_FU<br>?<br>7 2.40d |              |
|---|---|-------------------------------|---------------------|------------------------|--------------------|---------------------------|--------------|
| loading C<br>Loading U  | /Program  | Files (x8<br>: char_bt        | 6)/Sifo:<br>_pd.tcl | PDA608                 | /tcl/pd            | a600_1ibr                 | ary.thc      |
| More than<br>to connect   | one PDA w   | as detect<br>nstrument        | ed - you<br>to be u | MUST e                 | xecute a           | a 'pda de                 | v N' connand |
| WARNING: :<br>You cannot  | devices<br>use any  | cannot be<br>instrumen        | used.               | ?' valu                | es in tl           | he pda_co                 | nfig result. |
| If you dis<br>exit and p<br>PDA>pda de                          | cover an<br>re-start t                                      | instrumen<br>he shell         | t that n<br>to gain | access                 | o have f<br>to it. | AC power                  | turned on,   |
| PDA>pda_re<br>STATUS: RI<br>ALT A RES<br>ALT B RES<br>SIGNATURE | det count<br>EADY<br>ISTANCE: 2<br>ISTANCE: 2<br>_TYPE: SIN | 4.40 kohr<br>4.41 kohr<br>GLE | is<br>is            |                        |                    |                           |              |

Figure 19: PowerShell PDA Tcl Console

User written automated test scripts can run in either console, though if those scripts utilize Tk graphical user interface utilities such as message boxes, the Wish console must be used. Multiple PDA-604A instruments can be managed by scripts and commands executed in either PowerShell PDA console.

Every PDA command includes a standard convention to get help with command arguments, that is, valid argument forms and value ranges. A sampling of PowerShell PDA commands is presented in Table 3 below.

| <b>Resource Configuration</b> | Meter Commands | Utility Commands | Application Commands |
|-------------------------------|----------------|------------------|----------------------|
| pda_alt                       | pda_rdet       | pda_wait_meas    | pda_stream           |
| pda_polarity                  | pda_cdet       | pda_stop_meas    | pda_conformance      |
| pda source                    | pda class      | pda manage trace | pda Ildp             |
| pda link                      | pda ptrace     | pda update fw    | pda selftest         |



## PDA-600 Application Programming Interface

PDA-600 software, including PowerShell PDA and PDA Interactive, are built on top of a binary API library that can be accessed from any programming language able to link Windows DLL's and call Win32 functions. In many cases, there is a one-to-one relationship between PowerShell PDA commands such as those in Table 3 and underlying API calls accessible to other programming languages such as Microsoft Visual Basic, National Instruments LabView, or Python scripting language.

The binary API library is documented in the PDA-600 API Library Reference Manual furnished with the PDA-604A.

| Input / Output |   |  |
|----------------|---|--|
| Interface      | Parameter                                       | Specification                                    |
|                | Connections                                     | RJ45   |
|                | PoE Signaling and Supply Modes:                 | MODE A MDI, MODE A MDI-X, MODE B MDI,            |
|                | 2-Pair Operation                                | MODE B MDI-X                                     |
|                | PoE Signaling and Supply Modes:                 | MODE A MDI+ MODE B MDI,                          |
| PD Port        | 4-Pair Operation                                | MODE A MDI-X+ MODE B MDI,                        |
|                |   | MODE A MDI+ MODE B MDI-X,                        |
|                |   | MODE A MDI-X+ MODE B MDI-X                       |
|                | Data Rates and Signaling                        | 10/100/1000Base-T                                |
|                | Impedance                                       | 100 Ω, Balanced                                  |
| LAN Port       | Connections                                     | RJ45   |
|                | Modes   | Active Switched (for LLDP to PD) or Passive Thru |
|                | Data Rates and Signaling (Active Switched Mode) | 10/100/1000Base-T                                |
|                | Data Rates and Signaling (Passive Thru Mode)    | 10/100/1000/2.5G/5G/10GBase-T                    |
|                | Impedance                                       | 100 Ω, Balanced                                  |
| USB Port       | Connections                                     | USB Standard-B                                   |
|                | Туре  | USB 2.0 High Speed                               |
| User Interface | LED's   | USB: Connected, host is furnishing 5VDC          |
|                |   | LLDP: Blinks on to indicate LLDPDU received      |
|                |   | COM: Blinks when I/O from host occurs            |
|                |   | ALT A: DC Power Applied to Alt A pairs           |
|                |   | ALT B: DC Power Applied to Alt B pairs           |

## PDA-600 Technical Specifications

| Source Specific          | Source Specifications                           |   |  |  |  |  |
|--------------------------|---|---|--|--|--|--|
| Source                   | Parameter                                       | Specification   |  |  |  |  |
|                          | Output Voltage Range                            | 28 VDC to 57 VDC  |  |  |  |  |
|                          | Voltage Accuracy (50mA load)                    | ± (0.75% + 60 mV)   |  |  |  |  |
| DC Supply                | Voltage Resolution                              | 0.1 Volt  |  |  |  |  |
|                          | Source Resistance (typical)                     | 1.6 Ω   |  |  |  |  |
|                          | Maximum Continuous Source Current               | 1000 mA per Pairset, 2000mA total                                 |  |  |  |  |
| PD Detection Resistance  | Method  | ΔV / ΔΙ   |  |  |  |  |
|                          | Probing Voltage (typical)                       | 4.4 V – 8.8 V   |  |  |  |  |
|                          | Probing Range (Margin Test)                     | 2.7 V - 10.1 V  |  |  |  |  |
| PD Detection Capacitance | Method  | Slew Time   |  |  |  |  |
|                          | Probing Voltage (typical)                       | ~4 V – 8 V  |  |  |  |  |
| PD Classification        | Modes   | Standard: 1-Event or 2-Event                                      |  |  |  |  |
|                          |   | Type-3/4 License: 3-Event, 4-Event, and 5-Event                   |  |  |  |  |
|                          | First Event Duration                            | Selectable: T <sub>LCE</sub> 100 msec or T <sub>CEV</sub> 30 msec |  |  |  |  |
|                          | Classification Probing Voltage (typical)        | ~17.5 V   |  |  |  |  |
|                          | Classification Probing Voltage (margin test)    | 14.5 V, 20.5 V  |  |  |  |  |
|                          | Classification Probing Event Duration (typical) | 30 msec   |  |  |  |  |
|                          | Mark Region Voltage (typical, ≤ 6mA load)       | 7-9 V   |  |  |  |  |
|                          | Mark Region Duration (typical)                  | Mark 1 through 2, 3, or 4: 10 msec each                           |  |  |  |  |
|                          |   | Final Mark: Selectable, 25 to 375 msec                            |  |  |  |  |

| Measurement Specifications |  |               |  |  |
|----------------------------|--|---------------|--|--|
| Measurement                | Parameter  | Specification |  |  |
| Detection Resistance       | Range  | 3 KΩ to 50 KΩ |  |  |
| (per pairset)              | Accuracy (19 K $\Omega$ to 26.5 K $\Omega$ , Probing 4.4-8.8V) | ± 1%          |  |  |
|                            | Accuracy (Full Range, Probing 4.4-8.8V)                        | ± 2.5%        |  |  |

| Measurement Specifications |   |   |  |  |  |  |  |
|----------------------------|---|---|--|--|--|--|--|
| Measurement                | Parameter                                       | Specification   |  |  |  |  |  |
| Connection Check           | Outcomes  | SINGLE or DUAL or INVALID PD  |  |  |  |  |  |
|                            | Expected PD Signature for VALID result          | 21 K $\Omega$ to 28 K $\Omega$ on each pairset  |  |  |  |  |  |
| Detection Capacitance      | Range   | 50nF-10μF   |  |  |  |  |  |
| (per pairset)              | Accuracy (0.052 μF)                             | ± (2.5% + 6 nF)   |  |  |  |  |  |
|                            | Accuracy (2.110 µF)                             | ± (10% + 6 nF)  |  |  |  |  |  |
| Classification             | Classification Range                            | 0 mA to 50 mA   |  |  |  |  |  |
| (per pairset)              | Classification Accuracy (115 mA @ ~17.5V)       | ± (2.5% + 600 μA)   |  |  |  |  |  |
|                            | Classification Accuracy (1650 mA @ ~17.5V)      | ± (1.5% + 400 μA)   |  |  |  |  |  |
|                            | Events Measured                                 | Selectable, 1 to 2 (standard), 3-5 with Type-3/4 license  |  |  |  |  |  |
|                            | Mark Region Range                               | 0.5 to 5 mA   |  |  |  |  |  |
|                            | Mark Region Accuracy                            | ± (2% + 100 μA)   |  |  |  |  |  |
| Power                      | Range   | 0 to 56 Watts per pairset, 0 to 112 Watts 4-Pair  |  |  |  |  |  |
|                            | Resolution                                      | 0.01 W per pairset, 0.02 W 4-Pair   |  |  |  |  |  |
|                            | Accuracy  | ± (2.0% + 0.1 W) per pairset, ± (2.0% + 0.2 W) 4-Pair   |  |  |  |  |  |
| Load Current               | Range   | 0 to 1000 mA per pairset, 0 to 2000 mA 4-Pair   |  |  |  |  |  |
|                            | Resolution                                      | 0.1 mA, 0.2mA 4-Pair  |  |  |  |  |  |
|                            | Accuracy (115 mA)                               | ± (2.0% + 600 μA) per pairset, ± (2.0% + 1.2mA) 4-Pair  |  |  |  |  |  |
|                            | Accuracy (1650 mA)                              | ± (1.85% + 600 μA) per pairset, ± (1.85% + 1.2mA) 4-Pair  |  |  |  |  |  |
|                            | Accuracy (51100 mA)                             | ± (1.0% + 500 μA) per pairset, ± (1.0% + 1.0mA) 4-Pair  |  |  |  |  |  |
|                            | Accuracy (1011000 mA)                           | ± (0.75% + 800 μA) per pairset, ± (0.75% + 1.6mA) 4-Pair  |  |  |  |  |  |
| Port Voltage,              | Range   | 0 VDC to 57 VDC   |  |  |  |  |  |
| Backfeed Voltage with      | Resolution                                      | 0.1 V   |  |  |  |  |  |
| 2-Pair Powering            | Accuracy  | ± (0.75% + 100 mV) per pairset, ± (0.75% + 200 mV) 4-Pair   |  |  |  |  |  |
| Backfeed Voltage with      | Range   | TBD   |  |  |  |  |  |
| 3-Pair Powering            | Resolution                                      | TBD   |  |  |  |  |  |
|                            | Accuracy  | TBD   |  |  |  |  |  |
| Trace                      | Types   | Voltage, Current, Power, Voltage & Current (V+I)  |  |  |  |  |  |
|                            | Trigger Modes                                   | Immediate, Class (leading edge of first event),   |  |  |  |  |  |
|                            |   | Source (ON or OFF transition),  |  |  |  |  |  |
|                            |   | Transient (Current or Power) with Selectable (2-Pair)<br>Threshold and Selectable Pre-Trigger Sample Count  |  |  |  |  |  |
|                            | Sample Rate – 2-Pair Traces                     | 0.05 – 20 msec / sample (1-2-5 pattern)   |  |  |  |  |  |
|                            | (Immediate, Class, Source triggered traces)     | Voltage, Current also support 0.025 msec/sample   |  |  |  |  |  |
|                            | Sample Rate – 4-Pair Traces                     | 1 – 20 msec / sample (1-2-5 pattern)  |  |  |  |  |  |
|                            | (Immediate, Class, Source triggered traces)     |   |  |  |  |  |  |
|                            | Trace Length (Voltage, Current) – 2-Pair Traces | Selectable up to 5120 points  |  |  |  |  |  |
|                            | Trace Length (Voltage, Current) – 4-Pair Traces | Selectable up to 2560 points  |  |  |  |  |  |
|                            | Trace Length (Power, V+I) – 2-Pair Traces       | Selectable up to 2360 points  |  |  |  |  |  |
|                            | Sample Pate 2 Pair & 4 Pair Transient           | 1 20 msoc ( cample (1.2.5 pattern)  |  |  |  |  |  |
|                            | Triggered Traces                                | r – 20 msec / sample (1-2-5 patient)  |  |  |  |  |  |
|                            | Trace Length – Transient triggered traces       | Indefinite – Runs until specified <b>2-Pair</b> current or power<br>condition occurs. In 4-Pair mode, both pairsets are<br>monitored for the 2-Pair current or power condition. |  |  |  |  |  |
|                            | Trace Trigger Hold-off: Supported Triggers      | Class, Source   |  |  |  |  |  |
|                            | Trace Trigger Hold-off – 2-Pair Traces          | 0 to 65535 samples  |  |  |  |  |  |
|                            | Trace Trigger Hold-off – 4-Pair Traces          | 0 to 32768 samples  |  |  |  |  |  |
| Streaming Trace            | Parameters Included                             | Voltage, Current, Instantaneous Power, Avg. Power   |  |  |  |  |  |
| (2-Pair & 4-Pair modes)    | Sample Rate                                     | 5 msec or 10 msec   |  |  |  |  |  |
|                            | Trace Length (5 msec period)                    | ≤ 1048400 samples (< 5242 seconds)  |  |  |  |  |  |

| LLDP (802.3at TLV's)                      |                  |   |
|---|------------------|---|
| Interface                                 | Parameter        | Specification   |
| PD Port<br>(with LLDP-AT feature license) | Receive          | In-board Ethernet switch is configured to filter for LLDPDUs. Normally<br>parsed to extract the IEEE 802.3at conformant Power-via-MDI TLV;<br>entire raw frame is available for analysis. |
|   | Transmit         | LLDPDU containing an IEEE 802.3at conformant Power-via-MDI TLV<br>with programmatically controlled alloc value.   |
|   | Trace            | Continuous (once started by the user), stores and optionally displays<br>Power-via-MDI TLV content.   |
| LAN Port                                  | No LLDP support. |   |

| LLDP (802.3bt TLV's)                      |           |                           |
|---|-----------|---------------------------|
| Interface                                 | Parameter | Specification             |
| PD Port<br>(with LLDP-BT feature license) | Receive   | (TBD – Not yet available) |
|   | Transmit  | (TBD – Not yet available) |
|   | Trace     | (TBD – Not yet available) |

| Physical and Environment |                       |                                      |
|--------------------------|-----------------------|--------------------------------------|
| Measurement              | Parameter             | Specification                        |
| Physical                 | Width                 | 7.5"                                 |
|                          | Height                | 3.0"                                 |
|                          | Depth                 | 10.0"                                |
|                          | Weight                | 3.2 lbs                              |
|                          | Power                 | 100VAC – 240VAC, 50-60 Hz, 1.3A Max. |
| Environmental            | Operating Temperature | 0°C to 40°C                          |
|                          | Storage Temperature   | -20°C to 85°C                        |
|                          | Operating Humidity    | 5% to 95% RH, Non-Condensing         |
|                          | Altitude              | 2000 Meters                          |
|                          | Pollution Degree      | 2                                    |

| Certifications      |  |
|---------------------|--|
| Category            | Specification  |
| Safety              | CSA Listed (CSA22.2 No. 61010)   |
|                     | EN61010-1 (Test & Measurement Equipment Safety Standard)                 |
| Emissions           | FCC Part 15, Class A (Industrial Equipment emissions, USA)               |
|                     | EN55011 (Industrial, Scientific Equipment RF emissions, Europe)          |
|                     | VCCI (Information Technology Equipment emissions, Japan)                 |
|                     | AS/NZS 3548 (Information Technology Equipment emissions, Australia/N.Z.) |
| European Commission | Low Voltage Directive (2014/35/EU)                                       |
|                     | Electromagnetic Compatibility Directive (2014/30/EU)                     |
|                     | CE Marking Directive (93/68/EEC)   |
| Patents             | U.S. Patent 10,060,965   |

## **Ordering Information**

| PDA-604A<br>PDA-604-CT-AT | PDA-604A Instrument for 2-Pair Type-1 & Type-2 PD Analysis Including PDA-600 Software License for Automated <b>802.3at</b> PD Conformance Test Suite and Load Monitor applicable to Type-1 (up to 13W) and Type-2 (up to 25.5W) PD's |
|---------------------------|--|
| PDA-LLDP-AT               | License for 802.3at (Type-2) PD Powering and Analysis Using 802.3at LLDP.  |
| PDA-Type3/4*              | License for 802.3bt Type-3 (51W) and Type-4 (71.3W) PD Powering and Analysis Using 4-<br>Event and 5-Event Classification  |
| PDA-604-CT-BT*            | License for Automated <b>802.3bt</b> PD Conformance Test Suite and Load Monitor applicable to Type-3 (up to 51W) and Type-4 (up to 71.3W) PD's   |
| PDA-LLDP-BT*              | License for 802.3bt (Type-3/4) PD Powering and Analysis Using 802.3bt LLDP.  |
| RACKKIT-PDA               | Rack Mount Kit for PDA-600 Instruments (see below)   |
| CASE-PDA                  | Carrying Case for PDA-600 Instruments (see below)  |

#### \* Feature license not yet available

Accessories Included with PDA-604A:

- PDA-600 Reference Manual
- USB Cable
- PDA-600 Software (CD)
- Power Cord



Carrying Case for PDA-600



Rack Mount Kit for PDA-600

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